Agilent Technologies

Advanced Design System 2011.01

Feburary 2011 PDK Layout Verification

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# **PDK Layout Verification Tool**

The PDK Layout Verification Tool is an application that verifies PDK Layout components in the PDKs upgraded to ADS 2011 compatible format. This tool considers the ADS 2009U1 as the standard and compares the layout details of the component in ADS 2009U1 and ADS 2011.

The tool compares the following layout details:

- All shapes in all layers
  - Corresponding layer names
  - Corresponding layer purposes (assumes the layer purpose as drawing for the shapes drawn in ADS 2009U1)
  - Corresponding GDS II numbers
  - Coordinates of different shapes (radius and center in case of circle and arc)
- All pins
  - Corresponding pin location
  - Corresponding pin layers
  - Corresponding pin directions

The purpose of this tool is to allow users to easily verify their migrated PDKs and investigate for differences or errors (if any).

# **Pre-Requisites for Windows Operating System**

Before start comparing the two PDKs you need to check for the Pre-requisites for Windows Operating System. Following are the pre-requisites:

- Locally install ADS2009U1 and ADS2011\_01.
- Install PDK Layout Verification AddOn or Plugin (version 350.501) for 2009U1.
- Install WinMerge at the following location C:\ProgramFiles\WinMerge. This is the default location of the Winmerge installation.
  - \rm Note

Winmerge is a freeware easily available for download.

• The kit must contain both ATF and AEL files.

## Verifying ADS2009U1 and ADS2011\_01 PDKs

Following are the steps to open the PDK Layout Verification Tool and compare ADS2009U1 and ADS2011\_01 PDKs:

- 1. Click Start > All Programs > Advanced Design System 2011.01 > ADS Tools->PDK Layout Verification Tool to open the PDK Layout Verification Tool.
- 2. Click **File > New Project...** from the PDK Layout Verification Tool.

### Advanced Design System 2011.01 - PDK Layout Verification

	Help				
	/ Project :n Project	0			
	e As	ded PDK batible with ADS 2009 (	Jodate1 8	ADS 2011.	
C	:/users/default			Browse	Upload PDK
'Uj	oload PDK' installs	the upgraded PDK in A	NDS 2009	Update1 & AD:	5 2011
	e name.			Check!	Clear
Ed	lit Component I	List			
Se	lect/De-Select the	e PDK components		Edit Compo	onent List
	-	21 installation direc S 2009U1 database inf		Browse	Run
	<b>)5 2011 install</b> a un' generates AD	a <b>tion directory</b> S 2011 database infor	mation		
	:/Agilent/ADS201			Browse	Run

3. Type Project Name and click **OK**.

2.

New Project	? 🗙
Name	
C:/users/defaultxx/demokit_lvt	Browse
OK Cancel	Help

4. Click **Browse...** to select the PDK. The selected PDK should be compatible with both ADS 2009U1 and ADS 2011\_01.

PDK LVT ( C:/users/defaultxx/demokit_lvt* )
File Help
🌒 🌗 💾 🤪 🎯
Select the upgraded PDK Select the PDK compatible with ADS 2009 Update1 & ADS 2011.
(agilent/ADS2011_01/examples/DesignKit/DemoKit Browse) Upload PDK Upload PDK' installs the upgraded PDK in ADS 2009 Update1 & ADS 2011

5. Click **Upload PDK** to upload the Selected PDK.

Uploading the PDK would automatically install the PDK and set the technology of the upraged PDK in both ADS 2009U1 and ADS2011\_01. A confirmation message appears once the PDK is successfully uploaded.

	Progress	
5.	PDK Uploaded	
	ок	
	Note Ignore Step 6 to Step 8 if the uploaded PDK does	not include a Design Synchronized Artwork
	Select <b>Check If the PDK has Design Synd</b> Type the TechInclude component name.	chronised Artwork.

Check if the PDK has Design Synchronized Artwork Enter the name of the TechInclude Component below & press 'Check!' to verify the name.	
XYZ_TechInclude Check! Clear	
Note The TechInclude component name must be same as provide component.	d in the create_item of the TechInclud

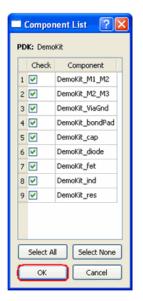
8. Click **Check!** to verify the name of the TechInclude component. If the name is correct and the component is present in the kit then a confirmation message is displayed.

🔲 Infor	mation 🔀
(	The provided TECH INCLUDE component name is correct!
	ОК

9. Click Edit Component List to Select or De-select the PDK components.

Edit Component List		
Select/De-Select the PDK components	Edit Component List	

10. Select the component from the component list as shown below.



Layout verification tool executes only for the selected componets. By default all the components are selected. Click **Select All** or **Select None** to select or de-select all the components in a single click.

- 11. Click  ${\bf OK}$  to finalize the component you need to verify.
- 12. Click **Run** to generate the ADS2009U1 database information.

12	ADS 2009 Update1 installation directory 'Run' generates ADS 2009U1 database information			
12.	C:/ADS2009U1 Browse	Run		

All the generated information is saved in a *.txt* format. A confirmation dialog box appears once the required database is generated and automatically exits ADS2009U1.

13. Click **Run** to generate the ADS2011\_01 database information.

ADS 2011 installation directory			
'Run' generates ADS 2011 database information			
C:/Agilent/ADS2011_01	Browse	Run	

All the generated information is saved in a .txt format. A confirmation dialog box appears once the required database is generated and automatically exits ADS2011 01.

14. Click **Compare Result** to view the details of the layout comparison for the two ADS versions. Following window shows the details and the result.

Component	ADS 2009U1Database file	ADS 2011 Database file	Status
1 DemoKit_M1_M2	/db_DSN/DemoKit_M1_M2.txt	/db_OA/DemoKit_M1_M2.txt	Pass
2 DemoKit_M2_M3	/db_DSN/DemoKit_M2_M3.txt	/db_OA/DemoKit_M2_M3.txt	Pass
3 DemoKit_ViaGnd	/db_DSN/DemoKit_ViaGnd.txt	/db_OA/DemoKit_ViaGnd.txt	Pass
4 DemoKit_bondPad	/db_DSN/DemoKit_bondPad.txt	/db_OA/DemoKit_bondPad.txt	Pass
5 DemoKit_cap	/db_DSN/DemoKit_cap.txt	/db_OA/DemoKit_cap.txt	Pass
6 DemoKit_diode	/db_DSN/DemoKit_diode.txt	/db_OA/DemoKit_diode.txt	Pass
7 DemoKit_fet	/db_DSN/DemoKit_fet.txt	/db_OA/DemoKit_fet.txt	Pass
8 DemoKit_ind	/db_DSN/DemoKit_ind.txt	/db_OA/DemoKit_ind.txt	Pass
9 DemoKit_res	/db_DSN/DemoKit_res.txt	/db_OA/DemoKit_res.txt	Pass

15. Click **View Error log** to view the errors encountered during the verification process. **Troubleshooting PDK Layout Verification Results** 

**Question:** What kind of results are expected in the final results window? **Solution:** Three kind of results are expected. Following are the results:

- Pass: Specifies that the component has no mismatches in the layout. You can safely assume that the component layout is exactly the same in both ADS 2009U1 and ADS 2011.
- Mismatch: Specifies that there are one or more than one mismatches in the component layout in ADS 2009U1 and ADS2011.

#### 🖯 Note

Mismatch does not necessarily mean that there is an error in ADS 2011. It might be an issue with ADS 2009U1 or with the PDK. So, we need to investigate the reason of mismatch before arriving to a conclusion.

• N.A: Specifies that the component could not be either placed in ADS 2009U1 or ADS 2011. Further investigation is required to reach to a conclusion.

Question: How do I get into the details of the comparison?

**Solution:** Click on the *Status* column of the component in the Comparison Result window. For example, if the result is Pass then click on Pass to open the *Winmerge* window:

### Advanced Design System 2011.01 - PDK Layout Verification

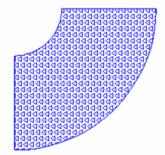
🚔 WinMerge - [DemoKit_M1_M2.txt - DemoKit_M1_M2.txt]						
	➢ File Edit View Merge Tools Plugins Window Help					
□ □ □ □ □ □ □ □						
	_mi_m2.bx - Demokut_mi_m2.bx C:\users\default000\demokit_ivt_03\db_DSN/Demokit_MI_M2.bxt	C:\users\default000\demokt_lvt_03\db_0A\Demokt_M1_M2.bxt				
ΠΠ	Pin direction: 2 Pin Layer: Metal2:drawing	Pin direction: 2 Pin Layer: Metal2:drawing				
	////Layer: Metal1:drawing GDSII: 4 Polygon: 1 Co-ordinates: -30,-30,-26,-26,28,28,32,32	////Layer: Metall:drawing GDSII: 4 Polygon: 1 Co-ordinates: -30,-30,-26,-26,28,28,32,32				
	////Layer: Vial:drawing GDSII: 5 Polygon: 1 Co-ordinates: -29.8,-29.8,-29.3,-29.3,28.3,28.3,28.8,28.8	////Layer: Vial:drawing GDSII: 5 Polygon: 1 Co-ordinates: -29.8,-29.8,-29.3,-29.3,28.3,28.3,28.8,2				
	Polygon: 2 Co-ordinates: -29.8,-29.8,-29.3,-29.3,29.3,29.3,29.8,29.8	Polygon: 2 Co-ordinates: -29.8,-29.8,-29.3,-29.3,29.3,29.3,29.8,2				
	Polygon: 3 Co-ordinates: -29.8,-29.8,-29.3,-29.3,30.3,30.3,30.8,30.8	Polygon: 3 Co-ordinates: -29.8,-29.8,-29.3,-29.3,30.3,30.3,30.8,5				
	Polygon: 4 Co-ordinates: -29.8,-29.8,-29.3,-29.3,31.3,31.3,31.8,31.8	Polygon: 4 Co-ordinates: -29.8,-29.8,-29.3,-29.3,31.3,31.3,31.8,3				
	Polygon: 5 Co-ordinates: -28.8,-28.8,-28.3,-28.3,28.3,28.3,28.8,28.8	Polygon: 5 Co-ordinates: -28.8,-28.8,-28.3,-28.3,28.3,28.3,28.8,2				
	Polygon: 6 Co-ordinates: -28.8,-28.8,-28.3,-28.3,29.3,29.3,29.8,29.8	Polygon: 6 Co-ordinates: -28.8,-28.8,-28.3,-28.3,29.3,29.3,29.8,2				
	Polygon: 7 Co-ordinates: -28.8,-28.8,-28.3,-28.3,30.3,30.3,30.8,30.8	Polygon: 7 Co-ordinates: -28.8,-28.8,-28.3,-28.3,30.3,30.3,30.8,5				
	Polygon: 8 Co-ordinates: -28.8,-28.8,-28.3,-28.3,31.3,31.3,31.8,31.8	Polygon: 8 Co-ordinates: -28.8,-28.8,-28.3,-28.3,31.3,31.3,31.8,5				
	Polygon: 9 Co-ordinates: -27.8,-27.8,-27.3,-27.3,28.3,28.3,28.8,28.8	Polygon: 9 Co-ordinates: -27.8,-27.8,-27.3,-27.3,28.3,28.3,28.8,2				
	Polygon: 10 Co-ordinates: -27.8,-27.8,-27.3,-27.3,29.3,29.3,29.8,29.8	Polygon: 10 Co-ordinates: -27.8,-27.8,-27.3,-27.3,29.3,29.3,29.8,2				
	Polygon: 11 Co-ordinates: -27.8,-27.8,-27.3,-27.3,30.3,30.3,30.8,30.8	Polygon: 11 Co-ordinates: -27.8,-27.8,-27.3,-27.3,30.3,30.3,30.8,5				

Question: Can I ignore any kind of a mismatch?

**Solution:** PDK LVT rounds off the coordinate value to second decimal place. So you might sometimes see the rounding off mismatched in the second decimal place. You can safely ignore such mismatches.

At times ADS 2009U1 returns fewer number of coordinates (than actually present) for a polygon. Ignore such mismatches

For example, Polygon shown in the below snapshot should have atleast 8 coordinates (4 points).



But in ADS 2009U1 the number of coordinates returned are 4 (2 points)See the figure below:

Polygon: 1	////Layer: ME1:drawing GDSII: 15 Polygon: 1 Co-ordinates: <mark>-30,-30,-20,0,20,40,50,50</mark>
Polygon: 1	////Layer: VIA2:drawing GDSII: 16 Polygon: 1 Co-ordinates: <mark>-30,-30,-19,-1,21,39,50,50</mark>